

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Serial No: 10/039,484 Confirmation No. 9755  
Applicant: Xiaowei Deng  
Filed: December 31, 2001  
Art Unit: 2826  
Examiner: Mondt, Johannes P.  
  
Docket No.: TI32801  
Customer No. 23494

**ELECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 or facsimile transmitted to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below.

*Cindy Dees*  
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*3-12-2004*  
Date

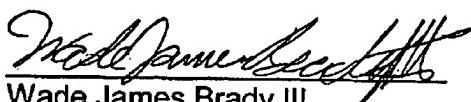
In response to the Office Action of December 16, 2003, Applicant elects Species 3 with traverse. Claims 1-46 are readable thereon. Contrary to the Examiner's statement that there are no generic claims, claims 1, 21, 26, and 36 are generic.

Applicant traverses the requirement to elect a single species on the grounds that the species identified by the Examiner are not patentably distinct species of the claimed invention. Claims 1-35 are directed to a method of determining a capacitance for use in a circuit simulation. Claims 36-46 are directed to an apparatus for simulating the operation of a circuit. Species 1 (page 8, line 21-page 10, line 26), as identified by the Examiner, is a transistor test structure (see Figure 2), and not a method of determining a capacitance or an apparatus for simulating the operation of a circuit and therefore not a species of the claimed invention. Species 2 (page 10, line 27-page 12, line 14), as identified by the Examiner, is a transistor design structure (see Figure 3), and not a method of determining a capacitance or an

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Amtd. Dated March 11, 2004  
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apparatus for simulating the operation of a circuit and therefore not a species of the claimed invention.

Respectfully submitted,



Wade James Brady III  
Registration No. 32,080  
Texas Instruments Incorporated  
(972) 917-4371

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